



GOVERNMENT OF KERALA

Abstract

Sree Chitra Thirunal College of Engineering - Annual plan 2021-22 - Setting up of new Nano electronics & Materials Lab in the Department of Electronics & Communication Engineering - Administrative Sanction accorded - Orders issued.

TRANSPORT (A) DEPARTMENT

G.O.(Rt)No.294/2021/TRANS Dated,Thiruvananthapuram, 03/09/2021

- Read 1 Proposal from the Principal (i/c), Sree Chitra Thirunal College of Engineering
- 2 Minutes of the 1st Departmental Working Group meeting for 2021-22 held on 30.04.2021.
- 3 Government letter No.TRANS-A3/90/2021-TRANS dated 29.07.2021
- 4 Letters No.92-A2/2021/SCTCE dated 25.06.2021, 29.06.2021 and 25.08.2021 from the Principal (i/c), Sree Chitra Thirunal College of Engineering
- 5 Minutes of meeting of Technical Committee held on 09.08.2021.

ORDER

The Departmental Working Group (Transport) held on 30.04.2021 has approved the proposal submitted by the Principal (i/c), Sree Chitra Thirunal College of Engineering for the setting up of new Nano electronics & Materials Lab in the Department of Electronics & Communication Engineering at an estimated cost of Rs. 1.00 Crore. The Technical Committee, after scrutinizing the estimates and specifications of items, recommended purchase of equipments for setting up of the new Nano electronics & Materials Lab, as per the minutes read as 5th paper above.

2. As approved by the Departmental Working Group and the

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93
09/09/21

Technical Committee, Government are pleased to accord administrative Sanction for the Establishment of a New Laboratory - Nano electronics & Materials lab in the Department of Electronics & Communication Engineering of the Sree Chitra Thirunal College of Engineering at an estimated cost of Rs.1 Crore (Rupees One Crore only) by meeting the expenditure under the Head of Account '5055-00-800-74 (P) - Academic Infrastructure Development - Sree Chitra Thirunal College of Engineering'. This administrative sanction includes the purchase of the following items:

1. Materials Synthesis Equipments
2. Device Fabrication Equipments
3. Device Characterization Equipments
4. Nano electronic Device Modeling Tool - COMSOL Multiphysics
5. Nano electronic Circuit Design and Optical Simulation tool - Cadence University Bundle, Optisim / Optigrating Simulation software
6. Optical equipments
7. Computational Facility - Desktop Computers i5 (20 Nos.), Gpu-Nvidia (1 No.), Desktop computer i9 (1 No.), Networking devices and UPS
8. Furniture
9. Air-conditioner (4 Nos.)
10. Consumables

3. Dr.Smitha P.S., Assistant Professor, Department of Electronics & Communication Engineering shall be the Project Manager and she shall ensure that the project will be completed within 31.03.2022.

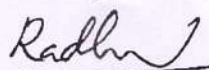
(By order of the Governor)
Vijayasree K S
Joint Secretary

The Principal (i/c), Sree Chitra Thirunal College of Engineering,
Pappanamcode, Thiruvananthapuram
Dr.Smitha P.S., Assistant Professor, Department of Electronics &
Communication Engineering, Sree Chitra Thirunal College of
Engineering, Pappanamcode, Thiruvananthapuram

(through the Principal, Sree Chitra Thirunal College of Engineering)
The Accountant General (Audit/A&E), Kerala, Thiruvananthapuram
The Sub Treasury Officer, Government Secretariat Sub treasury,
Thiruvananthapuram
Transport (D) Department
Planning & Economic Affairs Department
Finance Department
The Information & Public Relations (Web & New Media) Department
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Section Officer